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<p>(51) International classification :H02M0007538700, H02M0001420000, H02M0003158000, H02J0003400000, H02M0001150000</p> <p>(86) International Application No :PCT// Filing Date :01/01/1900</p> <p>(87) International Publication No : NA</p> <p>(61) Patent of Addition to Application Number :NA Filing Date :NA</p> <p>(62) Divisional to Application Number :NA Filing Date :NA</p>	<p>(71)Name of Applicant : 1)National Institute of Technology Karnataka Address of Applicant :Srinivasnagar PO, Surathkal, Mangalore - 575025, Karnataka, India. Mangalore -----</p> <p>Name of Applicant : NA Address of Applicant : NA</p> <p>(72)Name of Inventor : 1)Muthumula Rama Narayana Reddy Address of Applicant :# 1/3, Uppalur(V), Narasapuram (P), Kadapa-516217, Andra Pradesh, India Kadapa -----</p> <p>2)Bonthapalle Dastagiri Reddy Address of Applicant :1-127 B, Nelatur (Vi. &Po.), Duvvur, Kadapa-516175, Andhra Pradesh, India Duvvur -----</p> <p>-</p> <p>3)Prajof P Address of Applicant :No. 225, 3rd E Cross, HRBR Layout (III Block), Kalyan Nagar, Bengaluru-560043, Karnataka, India. Bangalore -----</p> <p>4)Dharavath Kishan Address of Applicant :# 2-59/2, Gaddigudem Thanda (Vi. & Po.), Mahabubabad-506101 Mahabubabad -----</p>
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(57) Abstract :

TITLE: Multiport High Gain Power Converter with Active Power Decoupling and its Operating Method Thereof ABSTRACT A single-phase multiport high gain converter with active power decoupling (101) comprising at least two DC ports (104 and 108) for receiving and delivering DC power; at least one AC port (106) for receiving and delivering AC power; at least one ripple port (110) for receiving and delivering ripple power; an active power decoupling unit (112) to compensate double frequency power ripple at the DC ports (104 and 108); a plurality of switches (S1 through S6) receiving power from at least one of the DC, AC, and ripple port (104, 108, 106, and 110) and processing the power; and a modulation scheme unit (114) to generate a modulating signal for operating the plurality of switches (S1 through S6) to control voltages and powers in at least one of the DC, AC and ripple port (104, 108, 106 and 110), wherein the modulation scheme unit (114) alters the modulating signal by determining DC offset (Dof1), AC voltage gain (MU), load angle δ , power factor angle Φ , ripple voltage amplitude (ML), phase shift angle (β) to achieve high voltage gain for both the AC and DC ports and further to eliminate double frequency power ripple at the DC ports (104 and 108).

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